

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L4	107112	touch near3 (panel or screen or sens\$6)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:38
L5	0	touch near3 (panel or screen or sens\$6) and (substrate adj sensor\$1) with (different or vary) adj voltages	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:40
L6	0	(substrate adj sensor\$1) with (different or vary) adj voltages	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:42
L7	0	(substrate adj sensor\$1) with (high adj voltage adj corner) and (low adj voltage adj corner)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:43
L8	0	(substrate adj sensor\$1) and (high adj voltage adj corner) and (low adj voltage adj corner)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:43
L9	2434771	(high adj voltage adj sensing or resist\$3) and (low adj voltage adj sensing or resist\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:58
L10	303560	(high adj voltage adj sensing or resist\$3) near5 (low adj voltage adj sensing or resist\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:46

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L11	19710	(high adj voltage adj sensing or resist\$3) near (low adj voltage adj sensing or resist\$3)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:46
L12	128	4 and 11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:30
L13	23706	(x adj coordinate\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:35
L14	46776	(y adj coordinate\$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:36
L15	42624	measur\$3 adj voltage\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:37
L16	31	13 with 15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:36
L17	45	14 with 15	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:36

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L18	0	(measur\$3 adj voltage\$1) with coordinate41	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:37
L19	189	(measur\$3 adj voltage\$1) with coordinate\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:37
L20	38	4 same 19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:38
L21	32	9 and 20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:52
L22	32	(high adj voltage adj sensing or resist\$3) and (low adj voltage adj sensing or resist\$3) and 20	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:57
L23	1	(high adj voltage) and (low adj voltage) and 22	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:01
L24	130	(high adj voltage adj sensing or resist\$3) and (low adj voltage adj sensing or resist\$3) and 19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:59

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L25	130	(high adj voltage adj sensing or resist\$3) same (low adj voltage adj sensing or resist\$3) and 19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 19:59
L27	52	(high adj voltage adj sensing or resist\$3) same (low adj voltage adj sensing or resist\$3) same 19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:00
L28	34	4 and 27	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:05
L29	330216	(high adj voltage) or (low adj voltage) and 28	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:02
L30	441522	(high adj voltage) or (low adj voltage)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:02
L31	2	28 and 30	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:02
L32	49975	first adj substrate\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:05

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L33	46290	second adj substrate\$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:06
L34	5	(resist\$3 adj sens\$3) with 32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:07
L35	7	(resist\$3 adj sens\$3) with 33	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:07
L36	83	(resist\$3 adj sens\$3) and 33	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:07
L37	69	(resist\$3 adj sens\$3) and 32	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:08
L38	57	36 and 37	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:08
L39	1	19 and 38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:09

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L40	0	17 and 38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:09
L41	6	15 and 38	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:11
L42	2	measur\$3 adj voltage\$1 same (sensor\$1 adj line\$1) same 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:29
L43	28	measur\$3 adj voltage\$1 same (sensor\$1 adj line\$1) and 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:18
L44	20	30 and 43	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:26
L45	4	32 same 33 and 11 and 4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:28
L46	3	32 same 33 with 11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:31

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L47	8	32 same 33 same 11	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:29
L48	0	measur\$3 adj voltage\$1 and 47	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:30
L49	23	32 same 33 and 13 same 14 and 10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:32
L50	5	4 and 49	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 20:32

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L3	0	((resistive adj sensing adj circuit) with (touch adj screen adj panel) and (resistive adj sensing adj circuit) with (sensor adj lines) with ("Xin") with ("Xout") with ("Yin") with ("Yout") and substrates with (symmetrically adj overlapped) and substrates with ("X or Y") with (direction) and (parallel and connection) with (sensor adj lines) with ("Xin") with ("Xout") with ("Yin") with ("Yout") and (parallel adj connections) with opposite adj corners) with (diagonal adj lines) with (touch adj screen adj panel) with (attain adj matching adj voltages) and (pair adj parallel adj connections) with (opposite adj corners) with (matching adj voltages) and (measured adj voltages) with (first adj corner adj connections) with (sensor adj lines) with (high adj voltages) and (measured adj voltages) with (second adj corner adj connections) with (low adj voltages) and (first adj corner adj connection) and (second adj corner adj connections) and (diagonal adj opposite adj corners) with (touch adj screen adj panel) and (voltage adj levels) with (corner adj points) with (one adj sensor adj line\$1) with ("not" adj comparison) with (sensor adj lines).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/12/21 18:34